

FIG.1 A

FIG.1 B

FIG.1 C

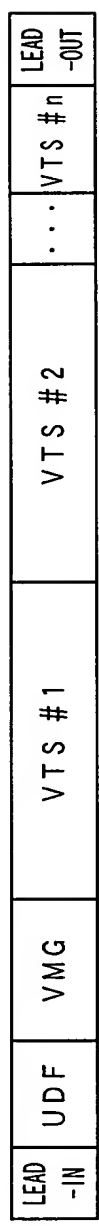


FIG.1 D

FIG.2A

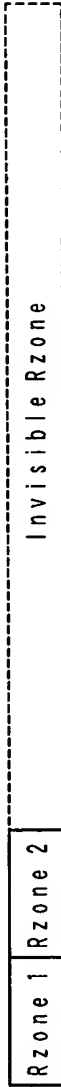


FIG.2B



FIG.2C



FIG.2D

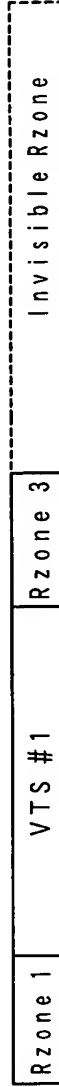


FIG.2E

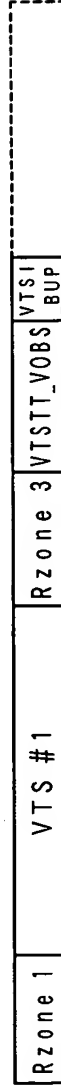


FIG.2F

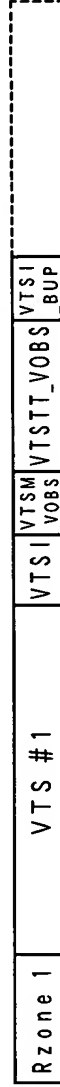


FIG.2G



...

FIG.2H



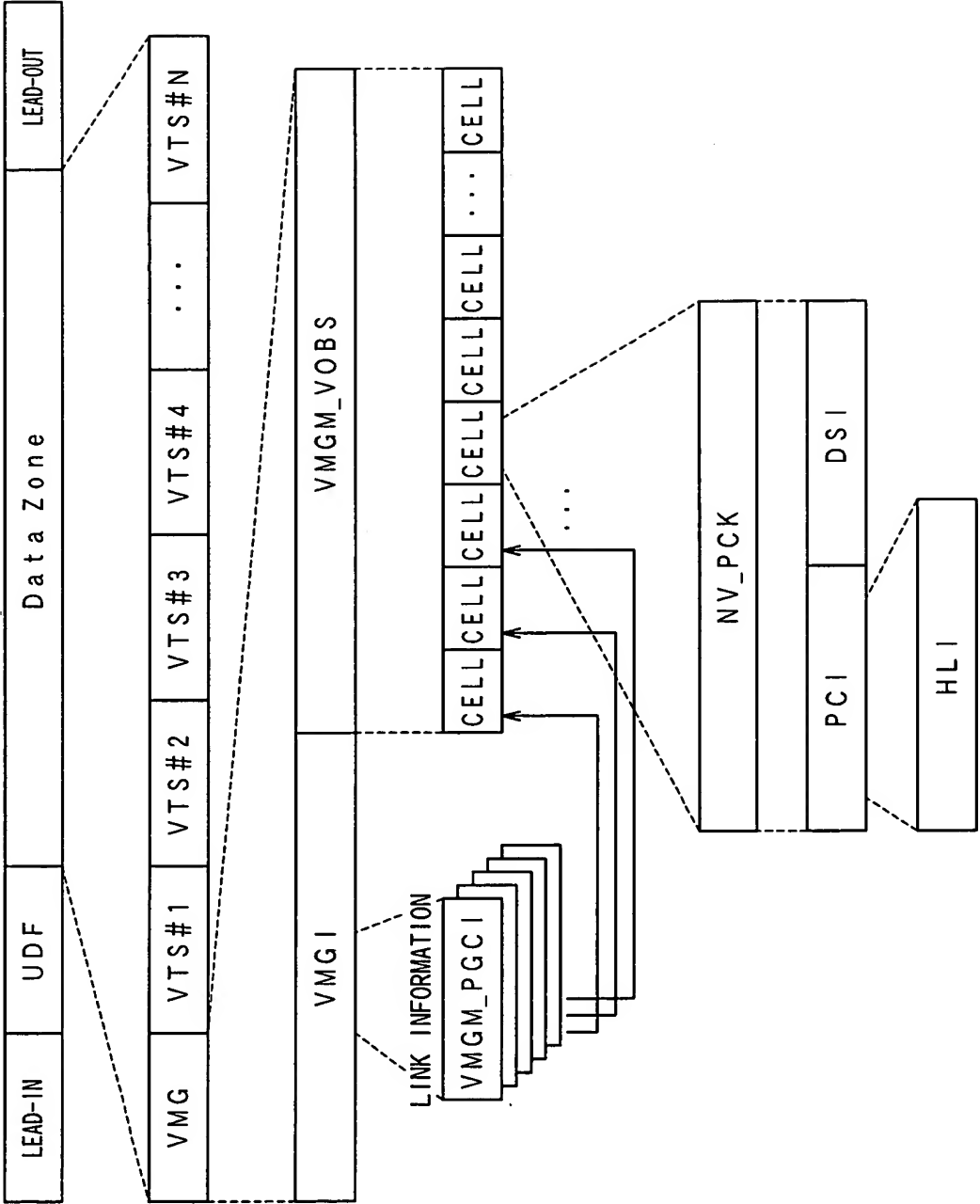


FIG.4

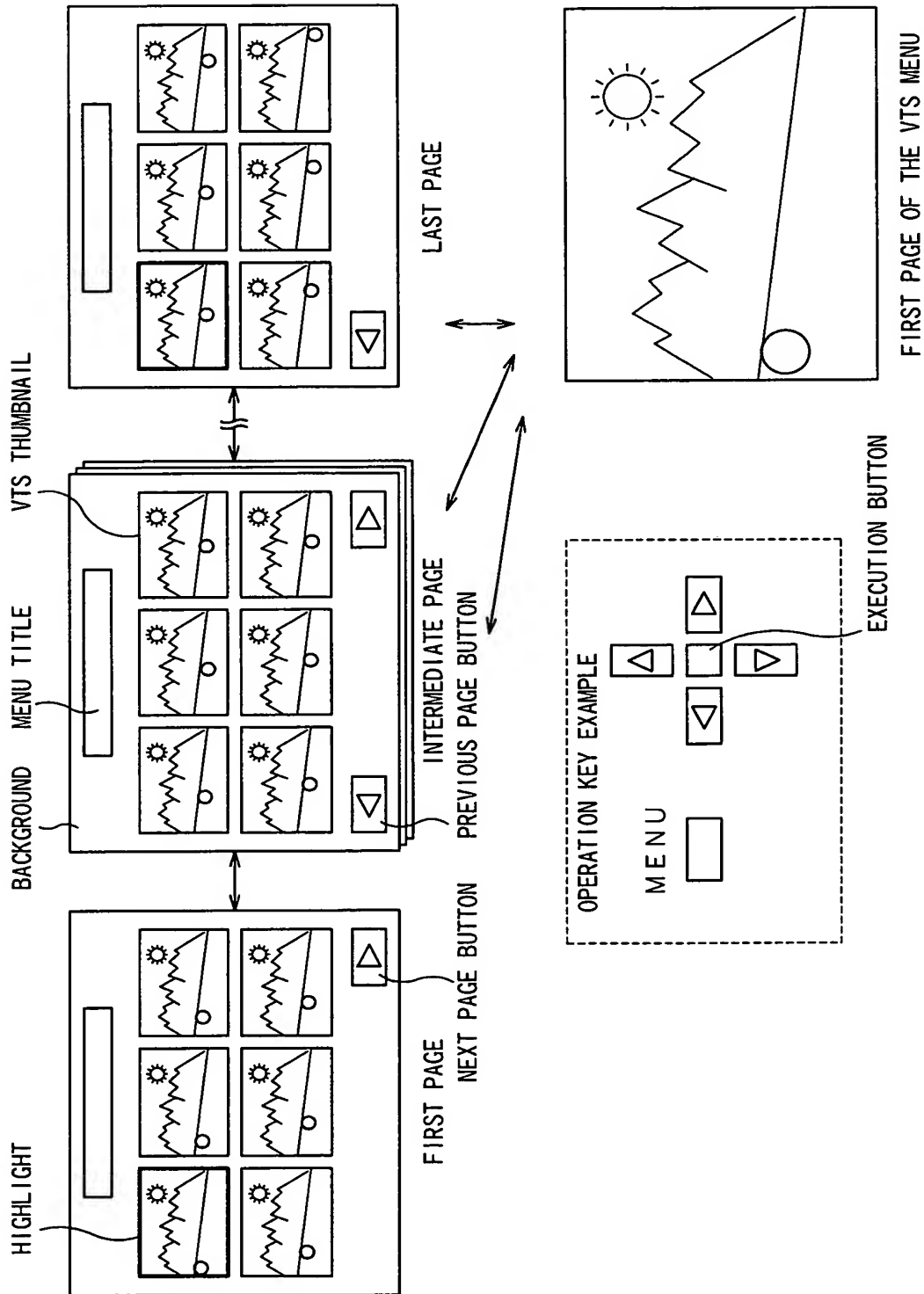


FIG.5

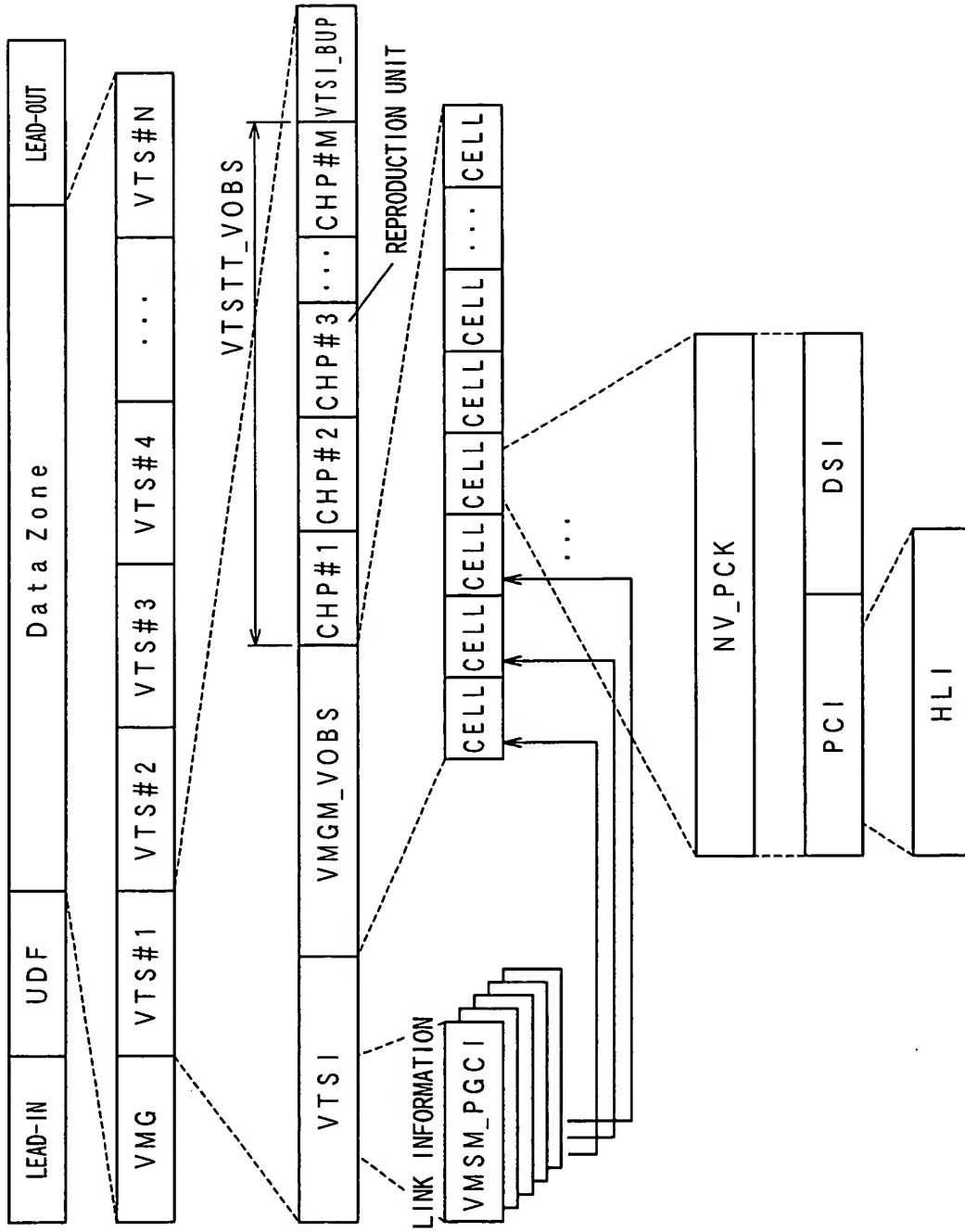


FIG.6

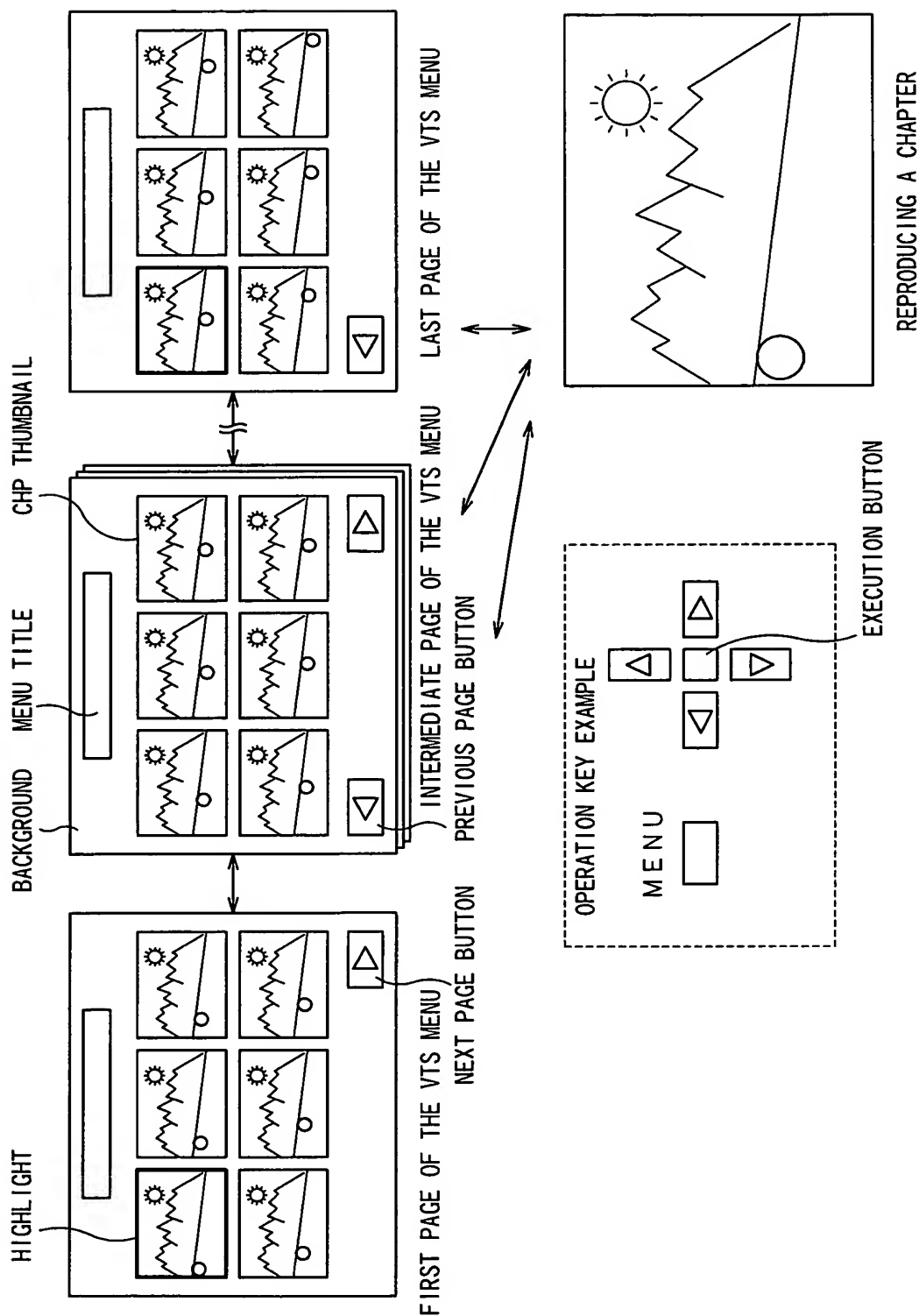


FIG.7

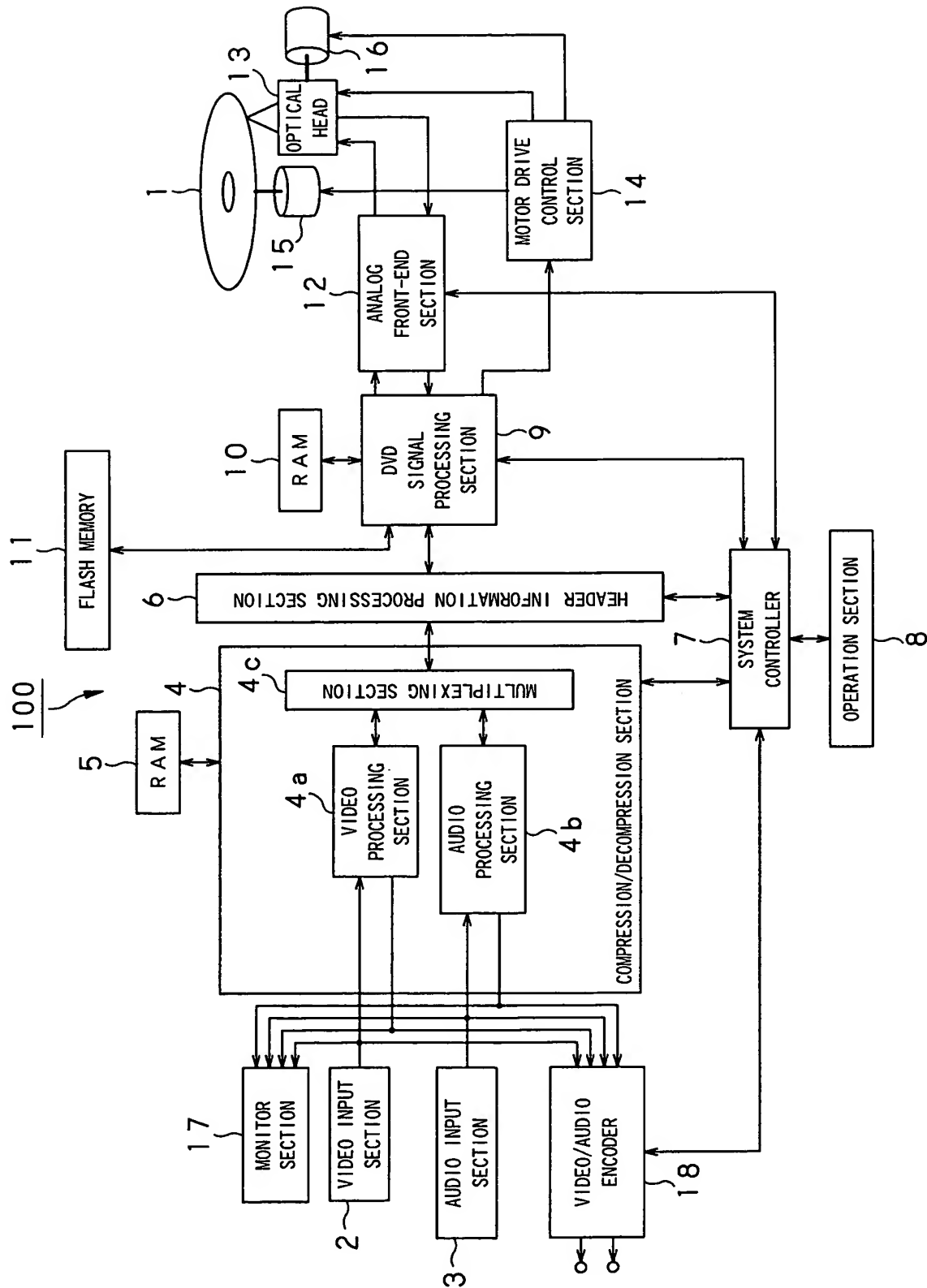


FIG. 8

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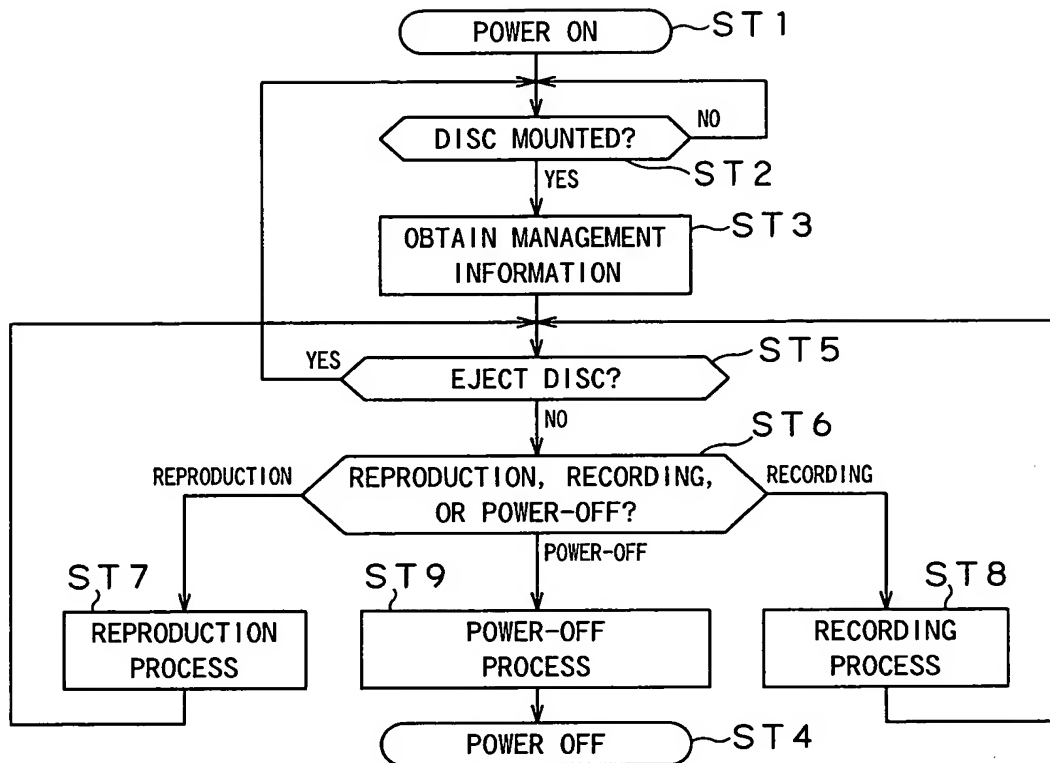


FIG. 9

Rzone 1 Rzone 2 Invisible Rzone

Rzone 1	Rzone 2	VTSTT_VOBS	VTSI_BUP

R zone 1	VTSL	TMP_VMGI	VTSTT_VOBS	VTSL_BUP

Rzone 1	VTS #1	Rzone 3	Invisible Rzone
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Rzone 1	VTS # 1	Rzone 3	VTSTT_VOBS	VTSL BUP
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Rzone 1	VTS # 1	VTSI	TMP_VMGI	VTSTT_VOBS	VTSI BUP

Rzone 1	VTS # 1	VTS # 2
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LEAD	UDF	VTS #1	VTS #2	...	VTS #n	LEAD -OUT
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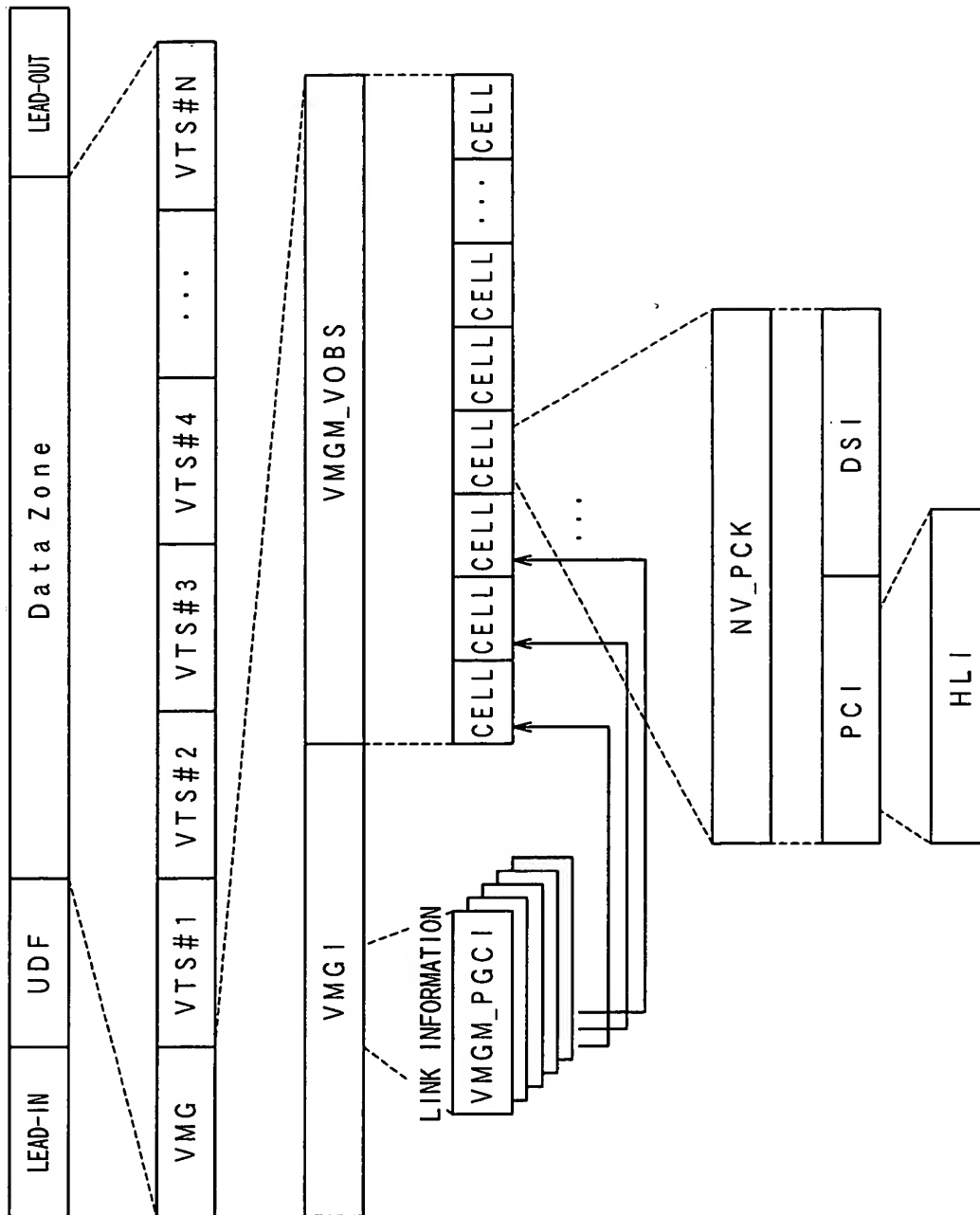


FIG. 11

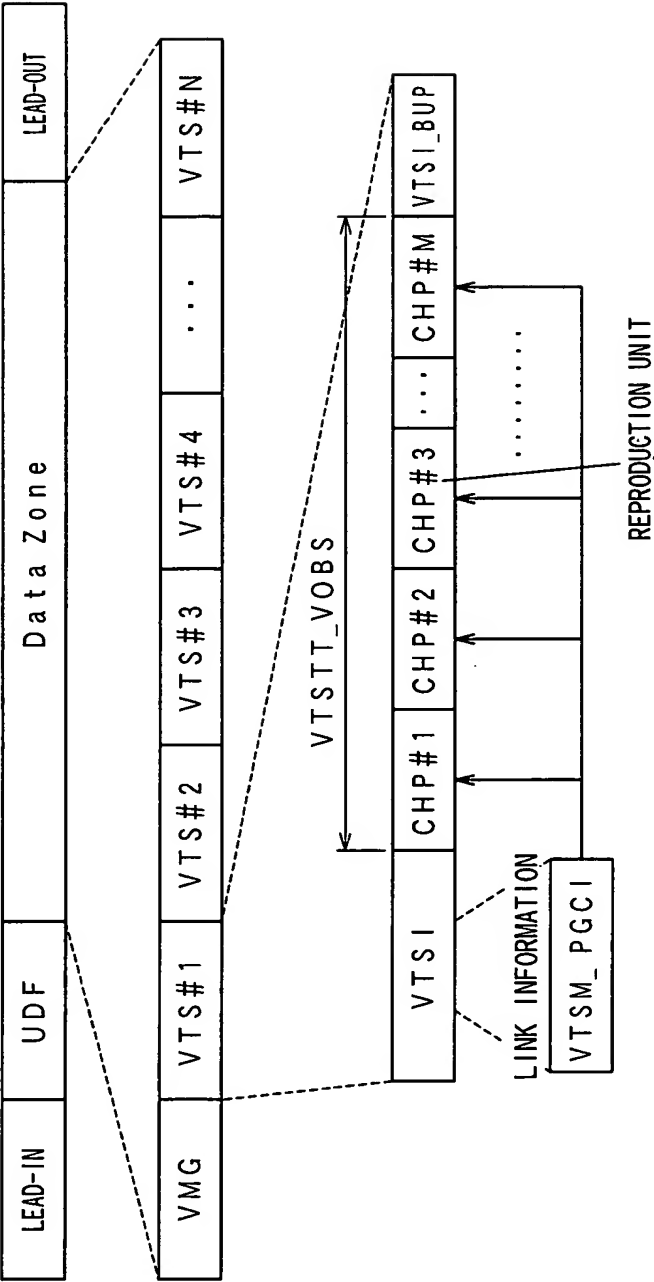


FIG.12

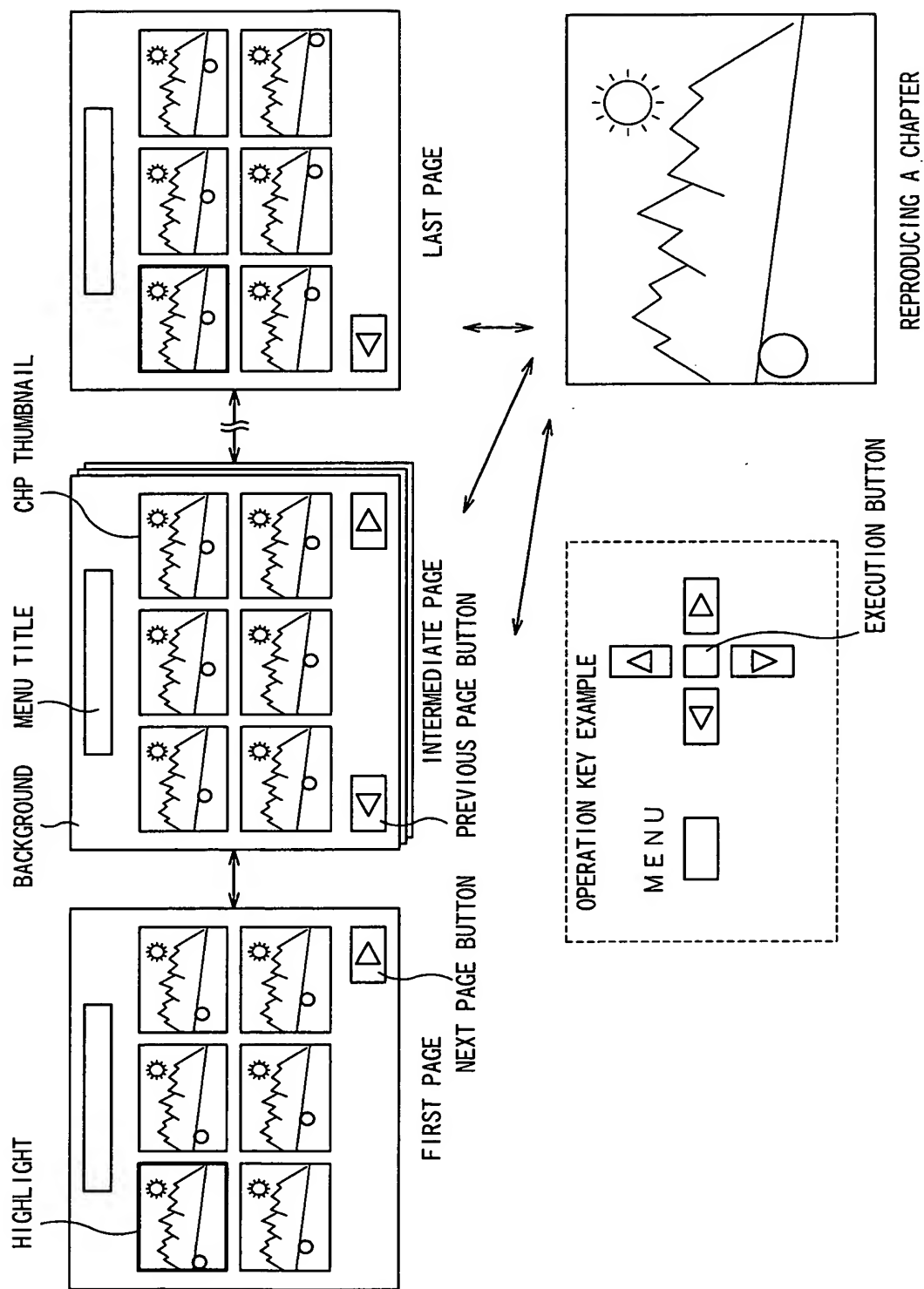


FIG.13

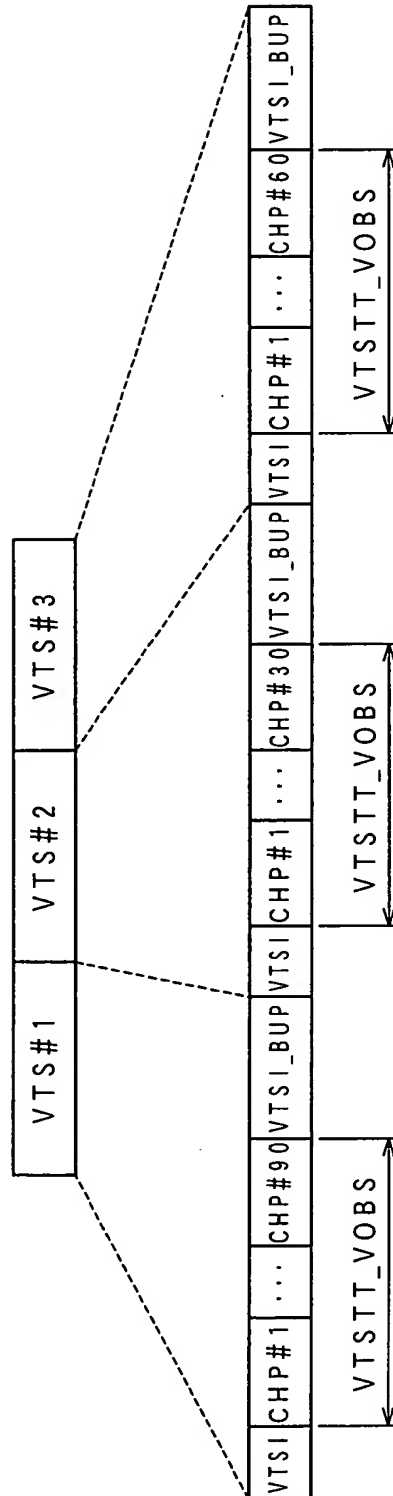


FIG. 14

FIG.15A	Padding									
FIG.15B	Padding		VTSTT_V08\$		VT\$1_BUP		VT\$1_BUP		VT\$1_BUP	
FIG.15C	Padd ing	TMP_ VMGI	Padd ing	VT\$1	VTSTT_V08\$	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP
FIG.15D	Padd ing	TMP_ VMGI	Padd ing	VTS #1		VTSTT_V08\$	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP
FIG.15E	Padd ing	TMP_ VMGI	Padd ing	VTS #1		VTSTT_V08\$	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP
FIG.15F	Padd ing	TMP_ VMGI	Padd ing	VTS #1		VTSTT_V08\$	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP	VT\$1_BUP
FIG.15G	LEAD -IN	UFD	TMP_ VMGI	VMG	VTS #1	VTS #2	...	VTS #N	LEAD -OUT	